



Sheet 1 of 2

**FORM PTO-1449  
INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT(S)**

Atty Docket No. : 12290/1  
Serial No. :  
Inventors : Y. MIYANAGA et al.  
Filed : 27 Dec. 2001  
Group Art Unit : 2641  
Examiner :

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**OTHER DOCUMENTS**

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1. Y. MIYANAGA et al.: *On Generalization and Adaptation of a Self-Organized Clustering*, Electronic Information/Communication Conference (A), Vol. J75-A, No. 7, pp. 1207-1215, July 1992 and English translation thereof
2. Y. MIYANAGA et al.: *On High Speed and High Accuracy Learning of a Network Using Self-Organization and Supervision*, Electronic Information/Communication Conference (A) Vol. J78-A, No. 11, pp. 1475-1484, Nov. 1995 and English translation thereof
3. Rafiqul ISLAM et al.: *Multi-clustering Network for Data Classification System*, IEICE TRANS. FUNDAMENTALS, Vol. E80-A, No. 9, pp. 1647-1654, Sep. 1997
4. Masahiro KONDA et al.: *Neuron-MOS Correlator Based on Manhattan Distance Computation for Event Recognition Hardware*, IEEE International Symposium on Circuits and Systems, Vol. 4, Atlanta, USA, pp. 217-220, May 1996

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5. Ugur Cilingiroflu et al.: *A 4-Transistor Euclidean Distance Cell for Analog Classifiers*, IEEE International Symposium on Circuits and Systems, Vol. 1, California, USA, pp. 84-87, May 1998

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6. M. KABASAWA et al.: *An Analog Design of a Clustering System by using C-matrix Structure*, Technical Report of IEICE, CAS2000-86 (2001-01), pp. 57-64

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7. M. KABASAWA et al.: *A Clustering/Labeling Analog LSI Architecture for Media Recognition*, Electronic Information/Communication Conference (A), Vol. J84-A, No. 4, pp. 456-461, April 2001

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